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**KEATING & BENNETT LLP**

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**To:** Examiner Berezny

**From:** Christopher A. Bennett

**Fax:** 703-872-9306

**Date:** August 27, 2004

**Phone:** 571-272-1686

**Pages:** 11

**Re:** 10/090,806

**CC:**

36856.648

**•Comments:**

Examiner Berezny,

Please find attached hereto the following documents for the above-identified application:

- Request for Reconsideration.

Respectfully submitted,



Christopher A. Bennett  
for  
Keating & Bennett, LLP  
(Registration Number 46,710)

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**CERTIFICATE OF FACSIMILE TRANSMISSION**

I hereby certify that this correspondence is being transmitted to  
Group Art Unit 2813, 703-872-9306, addressed to:  
Commissioner for Patents, P.O. Box 1450, Alexandria, VA  
22313-1450.

Date: August 27, 2004

Sonia V. McVean  
Sonia V. McVean

**PATENT**  
36856.648

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<b>Applicant:</b> Masahiko KAWASE et al.	<b>Art Unit:</b> 2813
<b>Serial No.:</b> 10/090,806	
<b>Filed:</b> March 6, 2002	<b>Examiner:</b> N. Berezny
<b>Title:</b> A CHIP-TYPE COMPOSITE ELECTRONIC COMPONENT AND MANUFACTURING METHOD THEREOF	

**REQUEST FOR RECONSIDERATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 8, 2004, please reconsider the  
above-identified application as follows:

**Listing of the Claims** begins on page 2. No amendments have been made to  
the Claims.

**Remarks/Arguments** begin on page 4 of this paper.